

Form PTO 1449 (Modified)		U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		ATTY DOCKET NO. 239415US2S		SERIAL NO. 10/602,618	
LIST OF REFERENCES CITED BY APPLICANT <div style="position: relative; width: 100px; height: 100px; margin: 0 auto;"> <div style="position: absolute; top: 0; left: 0; width: 100%; height: 100%; border: 2px solid black; border-radius: 50%; text-align: center; line-height: 100px; font-size: 1.2em; font-weight: bold;"> RECEIVED MAY 03 2005 PTO OFFICE 3345 KOF </div> </div>				APPLICANT Takanori TAMAI			
				FILING DATE June 25, 2003		GROUP 2825	
				U.S. PATENT DOCUMENTS			
EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE IF APPROPRIATE	
	AA						
	AB						
	AC						
	AD						
	AE						
	AF						
	AG						
	AH						
	AI						
	AJ						
	AK						
	AL						
	AM						
	AN						
FOREIGN PATENT DOCUMENTS							
		DOCUMENT NUMBER	DATE	COUNTRY	TRANSLATION YES NO		
<i>HW</i>	AO	WO 02/084538 A1	10/24/2002	WIPO			
	AP						
	AQ						
	AR						
	AS						
	AT						
	AU						
AV							
OTHER REFERENCES (Including Author, Title, Date, Pertinent Pages, etc.)							
<i>HW</i>	AW	Mark G. ARNOLD, et al., "A Synthesis Preprocessor that Converts Implicit Style Verilog Into One-Hot Designs", IEEE, VERILOG HDL Conference, March 31, 1997, pages 38-45					
	AX						
	AY						
	AZ						
Examiner <i>HW</i>					<input type="checkbox"/> Additional References sheet(s) attached Date Considered 9/12/05		
*Examiner: Initial if reference is considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.							